Amendments to the Specification

IN THE TITLE:

Please replace the current title, "Frequency Dividing Circuit," with <u>An Apparatus</u> for Frequency Dividing a Master Clock Signal by a Non-Integer.

IN THE CROSS-REFERENCE TO RELATED APPLICATIONS:

Please amend the specification to read as follows:

[0001] This application is a continuation of the U.S. Non-Provisional Application entitled "Frequency Dividing Circuit," Serial No. 10/307,308, filed December 2, 2002, now U.S. Patent No. 6,707,327, which is a continuation of U.S. Non-Provisional Application entitled "Frequency Dividing Circuit," Serial No. 09/982,844, filed October 22, 2001, now U.S. Patent No. 6,570,417, which claims the benefit of U.S. Provisional Application No. 60/248,425, filed November 14, 2000, all of which are incorporated herein in their entireties by reference.